



WP7113P3C Phototransistor

DESCRIPTION

- Made with NPN silicon phototransistor chips

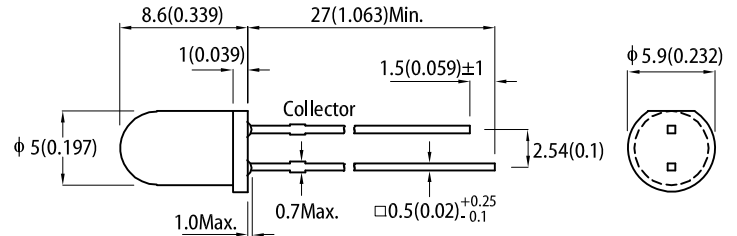
FEATURES

- Mechanically and spectrally matched to the infrared emitting LED lamp
- RoHS compliant

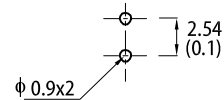
APPLICATIONS

- Infrared applied systems
- Optoelectronic switches
- Photodetector control circuits
- Sensor technology

PACKAGE DIMENSIONS



Recommended PCB Layout



Notes:

- All dimensions are in millimeters (inches).
- Tolerance is $\pm 0.25(0.01)$ unless otherwise noted.
- Lead spacing is measured where the leads emerge from the package.
- The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

ABSOLUTE MAXIMUM RATINGS at $T_A=25^\circ\text{C}$

Parameter	Max.Ratings	Units
Collector-to-Emitter Voltage	30	V
Emitter-to-Collector Voltage	5	V
Power Dissipation at (or below) 25°C Free Air Temperature	100	mW
Operating Temperature	-40 to +85	$^\circ\text{C}$
Storage Temperature	-40 to +85	$^\circ\text{C}$
Lead Soldering Temperature(>5mm for 5sec)	260	$^\circ\text{C}$

Note:

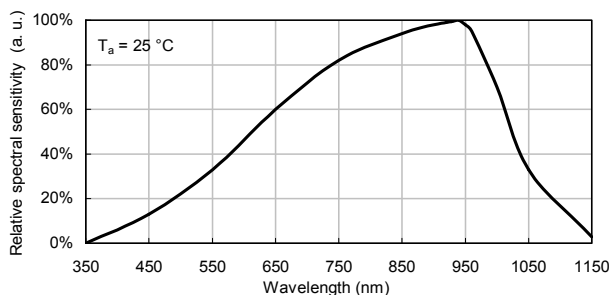
- Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

ELECTRICAL / OPTICAL CHARACTERISTICS at $T_A=25^\circ\text{C}$

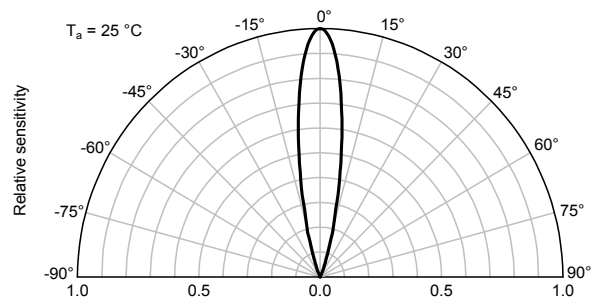
Parameter	Symbol	Min.	Typ.	Max.	Units	Test Conditions
Collector-to-Emitter Breakdown Voltage	$V_{BR\ CE0}$	30	-	-	V	$I_C = 100\mu\text{A}$ $E_e = 0\text{mW/cm}^2$
Emitter-to-Collector Breakdown Voltage	$V_{BR\ ECO}$	5	-	-	V	$I_E = 100\mu\text{A}$ $E_e = 0\text{mW/cm}^2$
Collector-to-Emitter Saturation Voltage	$V_{CE(SAT)}$	-	-	0.8	V	$I_C = 2\text{mA}$ $E_e = 20\text{mW/cm}^2$
Collector Dark Current	I_{CE0}	-	-	100	nA	$V_{CE} = 10\text{V}$ $E_e = 0\text{mW/cm}^2$
Rise Time(10% to 90%)	T_R	-	15	-	μS	$V_{CE} = 5\text{V}$ $I_C = 1\text{mA}$ $R_L = 1000\Omega$
Fall Time(90% to 10%)	T_F	-	15	-	μS	
On State Collector Current	$I_{(ON)}$	0.5	2.5	-	mA	$V_{CE} = 5\text{V}$ $E_e = 1\text{mW/cm}^2$ $\lambda = 940\text{nm}$
Range of spectral bandwidth	$\lambda_{0.1}$	420	-	1120	nm	-
Wavelength of peak sensitivity	λ_p	-	940	-	nm	-
Angle of half sensitivity	$2\theta_{1/2}$	-	20	-	deg	-

TECHNICAL DATA

RELATIVE SPECTRAL SENSITIVITY vs. WAVELENGTH

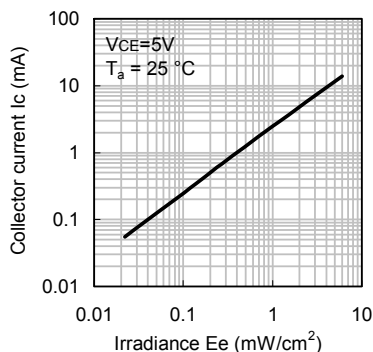


RELATIVE RADIANT SENSITIVITY vs. ANGULAR DISPLACEMENT

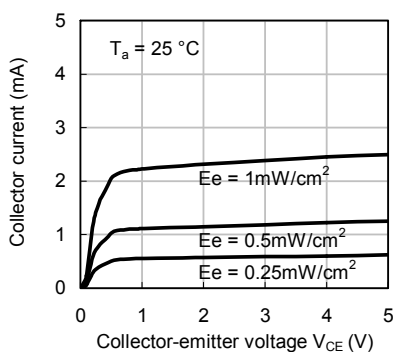


PHOTOTRANSISTOR

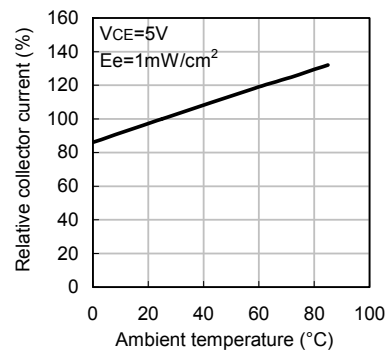
Collector Current vs. Irradiance



Collector Current vs. Collector-Emitter Voltage



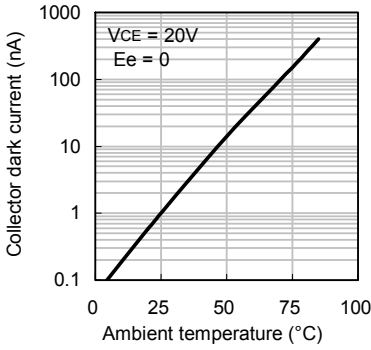
Relative Collector Current vs. Ambient Temperature



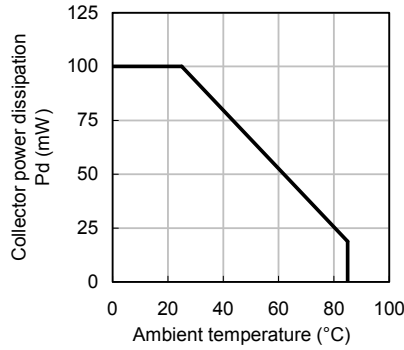
TECHNICAL DATA

PHOTOTRANSISTOR

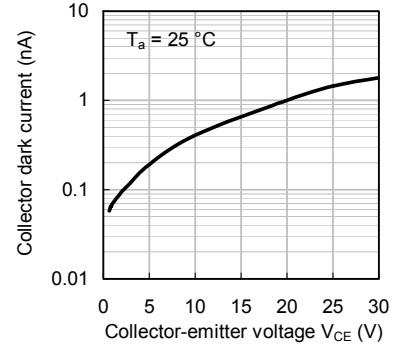
Collector Dark Current vs. Ambient Temperature



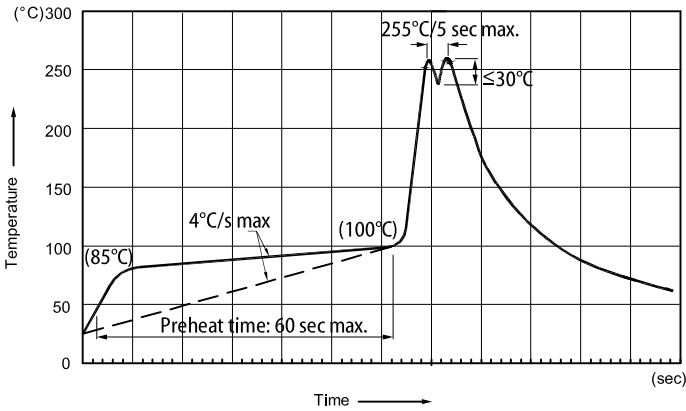
Collector Power Dissipation vs. Ambient Temperature



Collector Dark Current vs. Collector-Emitter Voltage



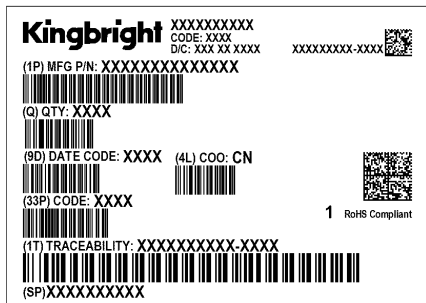
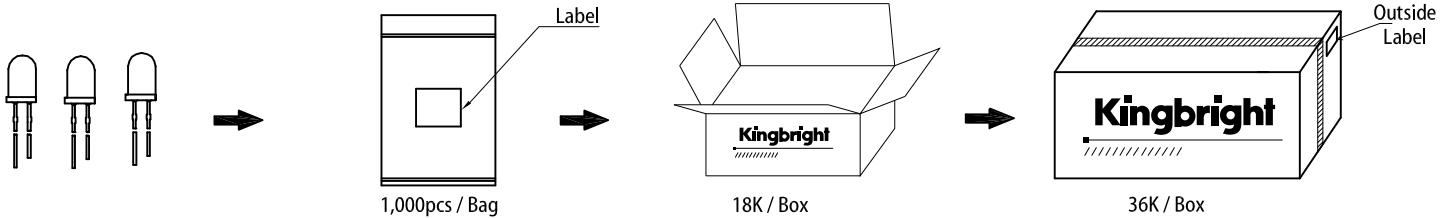
RECOMMENDED WAVE SOLDERING PROFILE



Notes:

1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C
2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
3. Do not apply stress to the epoxy resin while the temperature is above 85°C.
4. Fixtures should not incur stress on the component when mounting and during soldering process.
5. SAC 305 solder alloy is recommended.
6. No more than one wave soldering pass.

PACKING & LABEL SPECIFICATIONS



PRECAUTIONS

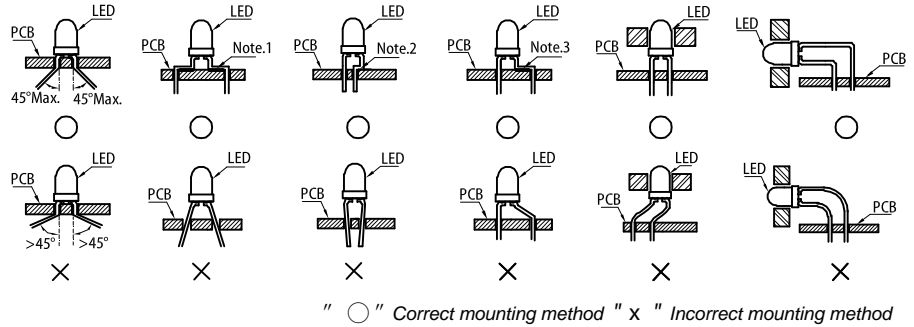
Storage conditions

1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
2. LEDs should be stored with temperature $\leq 30^{\circ}\text{C}$ and relative humidity $< 60\%$.
3. Product in the original sealed package is recommended to be assembled within 72 hours of opening.
Product in opened package for more than a week should be baked for 30 (+10/-0) hours at $85 \sim 100^{\circ}\text{C}$.

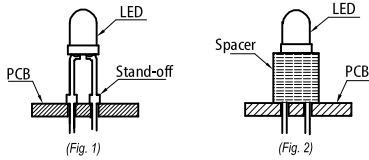
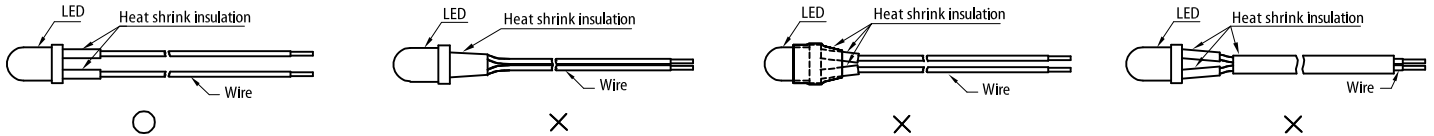
LED Mounting Method

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement.
Lead-forming may be required to insure the lead pitch matches the hole pitch.
Refer to the figure below for proper lead forming procedures.

Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.



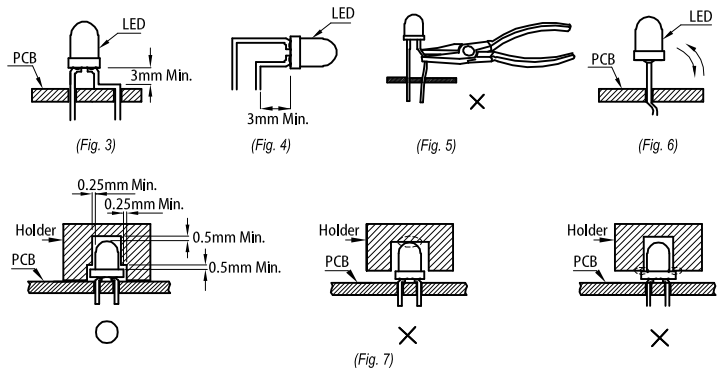
2. When soldering wires to the LED, each wire joint should be separately insulated with heat-shrink tube to prevent short-circuit contact. Do not bundle both wires in one heat shrink tube to avoid pinching the LED leads. Pinching stress on the LED leads may damage the internal structures and cause failure.



3. Use stand-offs (Fig. 1) or spacers (Fig. 2) to securely position the LED above the PCB.
4. Maintain a minimum of 3mm clearance between the base of the LED lens and the first lead bend (Fig. 3, Fig. 4).
5. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 5)

Lead Forming Procedures

1. Do not bend the leads more than twice. (Fig. 6)
2. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering. (Fig. 7)
3. The tip of the soldering iron should never touch the lens epoxy.
4. Through-hole LEDs are incompatible with reflow soldering.
5. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.



PRECAUTIONARY NOTES

1. The information included in this document reflects representative usage scenarios and is intended for technical reference only.
2. The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
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